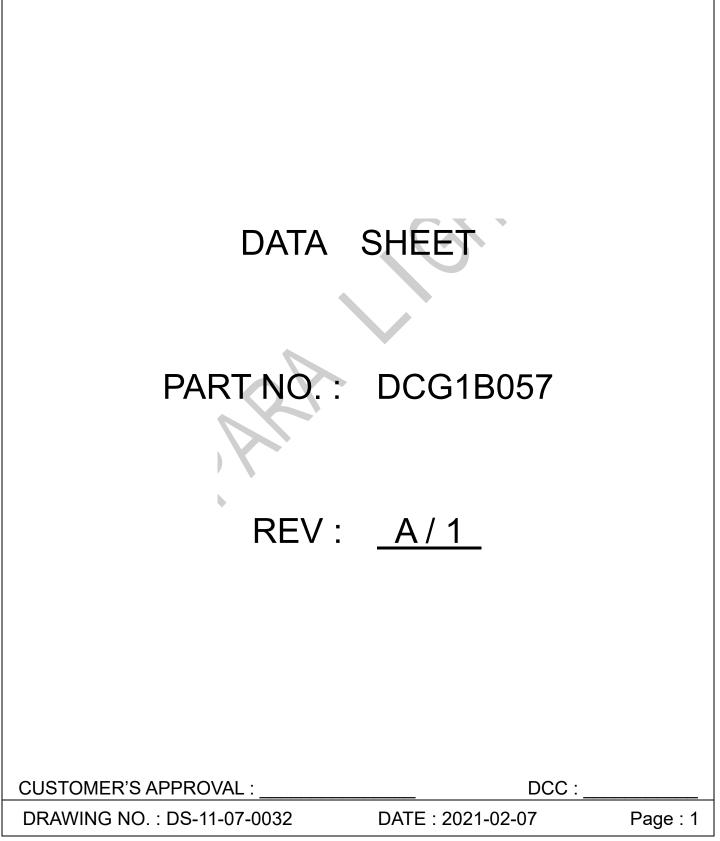


PARA LIGHT ELECTRONICS CO., LTD.

11F., No. 8, Jiankang Rd., Zhonghe Dist., New Taipei City 235, Taiwan,Tel: 886-2-2225-3733Fax: 886-2-2225-4800E-mail: para@para.com.twhttp://www.para.com.tw

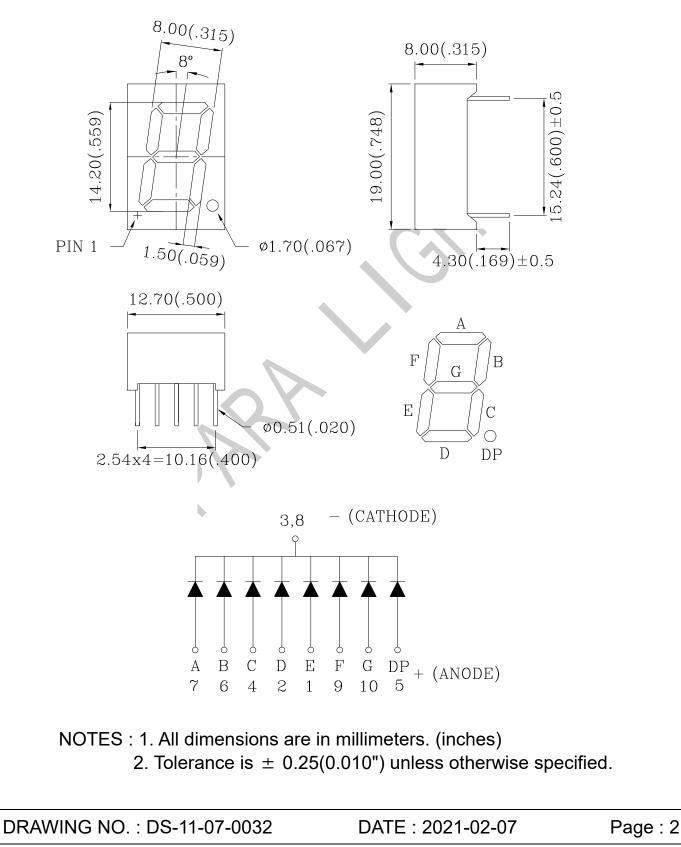




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REV:A/1

PACKAGE DIMENSIONS





DCG1B057

REV:A/1

FEATURES

- * 14.20mm (0.56 inch) HIGH HEIGHT
- * COMMON CATHODE
- * I.C. COMPATIBLE
- * LOW POWER CONSUMPTION
- * Pb FREE PRODUCTS
- * GRAY FACE, WHITE SEGMENTS

Raw Material : GalnN/GaN

ABSOLUTE MAXIMUM RATING : (Ta = 25°C)

PARAMETER	ULTRA BLUE	UNIT		
Power Dissipation Per Segment	40	mW		
Reverse Voltage Per Segment	5	V		
Continuous Forward Current Per Segment	20	mA		
Peak Forward Current Per Segment	60	m ^		
(1/10 Duty Cycle,0.1ms Pulse Width)	00	mA		
Electrostatic Discharge Threshold (HBM)	1000	V		
Operating Temperature Range - 35°C to 85°C				
Storage Temperature Range	- 35°C to 85°C			
	Power Dissipation Per Segment Reverse Voltage Per Segment Continuous Forward Current Per Segment Peak Forward Current Per Segment (1/10 Duty Cycle,0.1ms Pulse Width) Electrostatic Discharge Threshold (HBM) Operating Temperature Range	Power Dissipation Per Segment40Reverse Voltage Per Segment5Continuous Forward Current Per Segment20Peak Forward Current Per Segment60(1/10 Duty Cycle,0.1ms Pulse Width)60Electrostatic Discharge Threshold (HBM)1000Operating Temperature Range- 35°C to 85		

ELECTRO-OPTICAL CHARACTERISTICS : (Ta = 25°C)

	SYMBOL	. PARAMETER		TEST CO	ONDITION	MIN	. TYP.	MAX	Χ.	UNIT	
	VF	Forward Voltage ,Any Segment		IF =	10mA		3.0	3.5	5	V	
	lr	Reverse Current ,Any Segment		VR	= 5V			10		μA	
	λΡ	 λP Peak Emission Wavelength λD Dominant Wavelength Δλ Spectral Line Half - Width 		IF =	10mA		465			nm	
	λD			IF =	10mA		470			nm	
	Δλ			IF =	10mA		30			nm	
	١v	Luminous Int	ensity Per Segn	nent	IF =	10mA	11.2	2 28.0			mcd
В	BIN CODE (IF = 10mA)										
	Dice Bin		J		K	L		М			Ν
	l⊭(mcd)	11.5-16.72	16.73-23.17	23.	18-30.86	30.87-42.	95	42.96-57	7.84	57	7.85-65.

TOLERANCE IS: $\pm 15\%$

DRAWING NO. : DS-11-07-0032

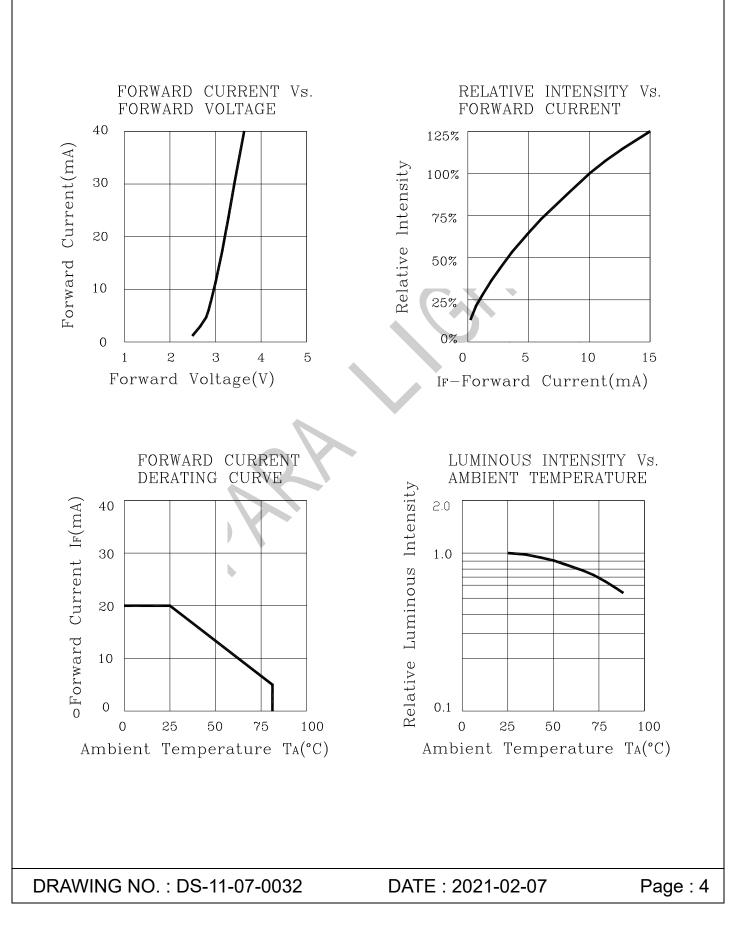
DATE : 2021-02-07

Page: 3



DCG1B057

REV:A/1



PARA ight

0.56 INCH SINGLE DIGIT DISPLAY

DCG1B057

REV:A/1

•SOLDERING

METHOD	SOLDERING CONDITIONS	REMARK
DIP SOLDERING	Bath temperature: 260 max Immersion time: with 5 sec	 Solder no closer than 2mm from the base of the package Using soldering flux," RESIN FLUX" is recommended.
SOLDERING IRON	Soldering iron: 30W or smaller Temperature at tip of iron: 260℃ or lower Soldering time: within 5 sec.	 During soldering, take care not to press the tip of iron against the PIN. (To prevent heat from being transferred directly to the PIN.)

1) When soldering the PIN of Display in a jig that the package is fixed with a panel (See flg.1), be careful not to stress the PIN with iron tip. When soldering Display in a condition that the package is fixed with a panel, be careful not to cling and stress the surface of Display on the panel to avoid damaging the Display.

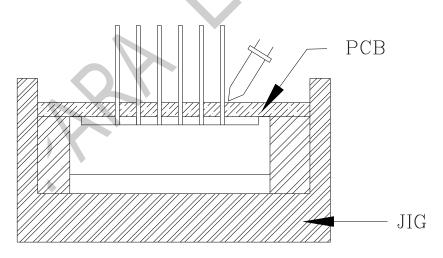


Fig.1

Regarding solution in the tinning oven for product-tinning, compound sub-solution made of tin & copper and silver is proposed with the temperature of Celsius 260. The proportion of the alloyed solution is tin 95.5: copper 3.5: silver 0.5 by percentage. The time of tinning is constantly 3 seconds.

DRAWING NO. : DS-11-07-0032

DATE : 2021-02-07



DCG1B057

REV:A/1

2) Similarly, when a jig is used to solder the Display to PC board, take care as much as possible to avoid steering the PIN (See Fig.2).

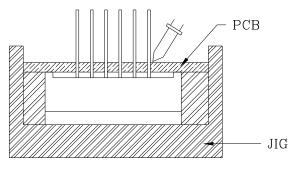
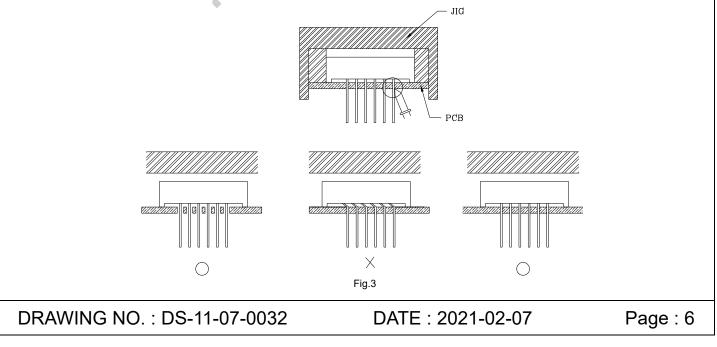


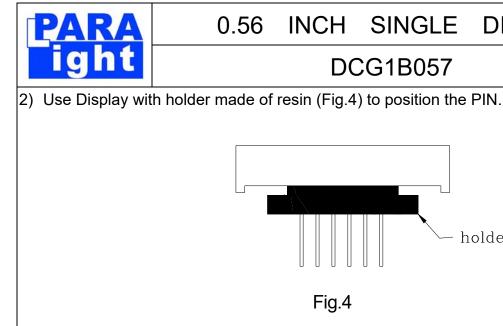
Fig.2

- 3) Repositioning after soldering should be avoided as much as possible. If inevitable, be sure to preserve the soldering conditions with irons stated above: select a best-suited method that assures the least stress to the Display.
- PIN cutting after soldering should be performed only after the Display temperature has returned to normal temperature.

•LED MOUNTING METHOD

1) When mounting the Display by using a case, as shown Fig.3, ensure that the mounting holds on the PC board match the pitch of the PIN correctly-tolerance of dimensions of the respective components including the Display should be taken into account especially when designing the case, PC board, etc. to prevent pitch misalignment between the PIN and board holes, the diameter of the board holes should be slightly larger than the size of the PIN. Alternatively, the shape of the holes should be made oval. (See Fig.3)





•FORMED LEAD

1) The PIN should be bent at a point located at least 2mm away from the package. Bending should be performed with base fixed means of a jig or pliers (Fig.5)

Fig.4

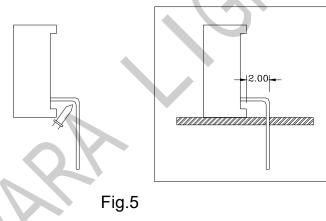
SINGLE

holder

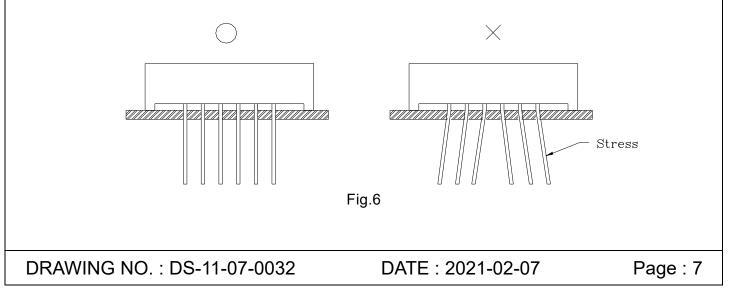
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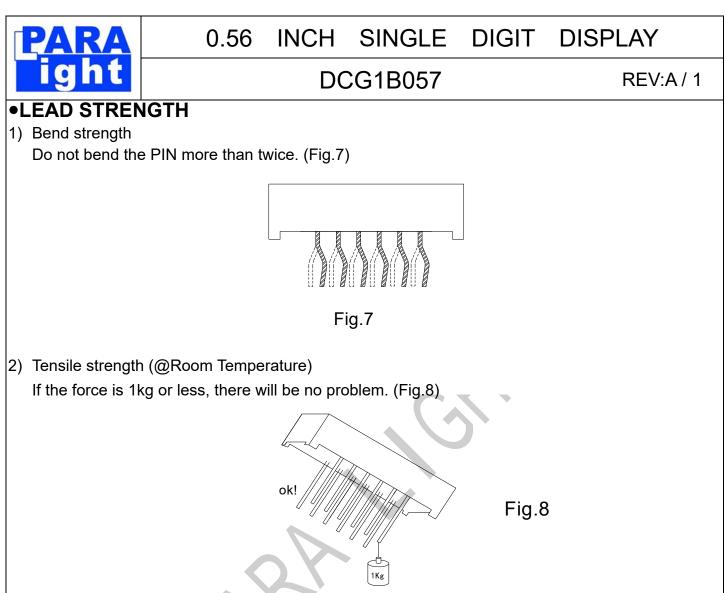
DIGIT DISPLAY

REV:A/1



- 2) Forming PIN should be carried our prior to soldering and never during or after soldering.
- 3) Form the PIN to ensure alignment between the PIN and the hole on board, so that stress against the Display is prevented. (Fig.6)





•HANDLING PRECAUTIONS

Although rigid against vibration, the Display may damaged or scratched if dropped. So take care when handling.

• STORAGE

- The Display should be stored at 30°C or less and 70% RH or less after being shipped from PARA and the storage life limits are 3 months.
- 2) PARA Display lead frames are comprised of a stannum plated iron alloy. The silver surface may be affected by environments which contain corrosive gases and so on. Please avoid conditions which may cause the Display to corrode, tarnish or discolor. This corrosion or discoloration may cause difficulty during soldering operations. It is recommended that the Display be used as soon as possible.
- 3) Please avoid rapid transitions in ambient temperature, especially, in high humidity environments where condensation can occur.

DATE : 2021-02-07



DCG1B057

REV:A/1

• HEAT GENERATION

- 1) Thermal design of the end product is of paramount importance. Please consider the heat generation of the Display when making the system design. The coefficient of temperature increase per input electric power is affected by the thermal resistance of the circuit board and density of Display placement on the board, as well as other components. It is necessary to avoid intense heat generation and operate within the maximum ratings given in this specification.
- The operating current should be decided after considering the ambient maximum temperature of Display .

•OTHERS

- 1) Care must be taken to ensure that the reverse voltage will not exceed the absolute maximum rating when using the Display with matrix drive.
- 2) Flashing lights have been known to cause discomfort in people; you can prevent this by taking precautions during use. Also, people should be cautious when using equipment that has had Display incorporated into it.
- 3) The Display described in this brochure are intended to be used for ordinary electronic equipment (such as office equipment, communications equipment, measurement instruments and household appliances). Consult PARA's sales staff in advance for information on the applications in which exceptional quality and reliability are required, particularly when the failure or malfunction of the Display may directly jeopardize life or health (such as for airplanes, aerospace, submersible repeaters, nuclear reactor control systems, automobiles, traffic control equipment, life support systems and safety devices).
- 4) User shall not reverse engineer by disassembling or analysis of the Display without having prior written consent from PARA. When defective Display are found, the User shall inform PARA directly before disassembling or analysis.
- 5) The formal specifications must be exchanged and signed by both parties before large volume purchase begins.
- 6) The appearance and specifications of the product may be modified for improvement without notice.

DATE : 2021-02-07



DCG1B057

REV:A/1

• STATIC ELECTRICITY

- Static electricity or surge voltage damages the Display.
 It is recommended that a wrist band or an anti-electrostatic glove be used when handling the Display.
- 2) All devices, equipment and machinery must be properly grounded. It is recommended that measures be taken against surge voltage to the equipment that mounts the Display.
- 3) When inspecting the final products in which Display were assembled, it is recommended to check whether the assembled Display are damaged by static electricity or not. It is easy to find static-damaged Display by a light-on test or a VF test at a lower current (below 1mA is recommended).
- 4) Damaged Display will show some unusual characteristics such as the leak current remarkably increases, the forward voltage becomes lower, or the Display do not light at the low current.
 Criteria : (VF>2.0V at IF=0.5mA)

•WAVE SOLDER PROFILE

